

# CH641 Evaluation Board Reference

Version: V1.1

<https://wch-ic.com>

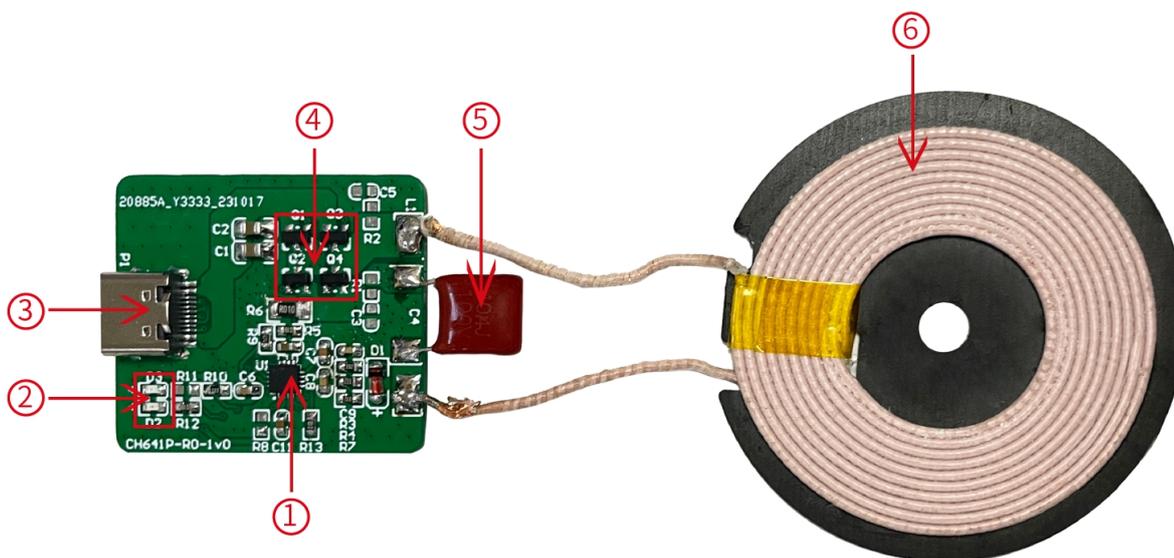
## 1. Overview

This evaluation board is applied to the development of the CH641 chip. The IDE uses the MounRiver compiler, with the option of WCH-Link for emulation and download, and provides reference examples and demonstrations of applications related to chip resources.

## 2. Evaluation Board Hardware

Please refer to the CH641SCH.pdf document for the schematic of the evaluation board.

CH641 Evaluation Board



Description

- |                         |                           |                    |
|-------------------------|---------------------------|--------------------|
| 1. Main control MCU     | 2. LED                    | 3. USB-C interface |
| 4. Bridge drive circuit | 5. CBB resonant capacitor | 6. Coil            |

The above CH641 evaluation board comes with the following resources.

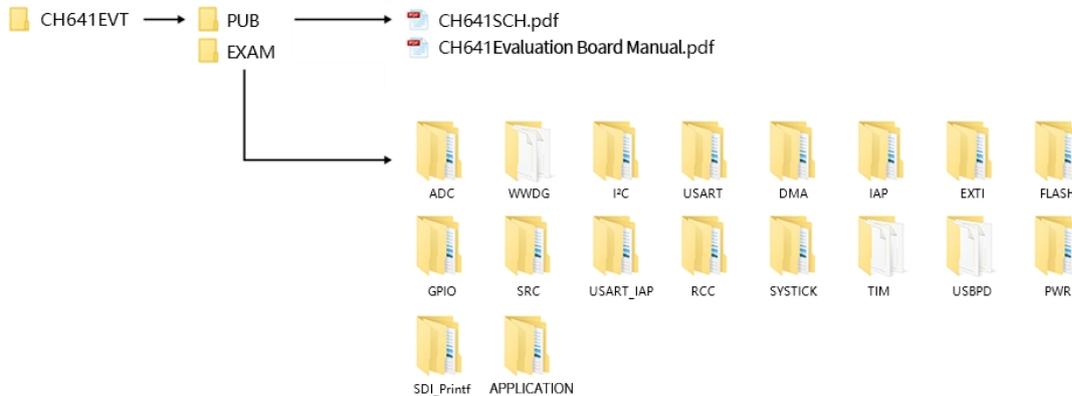
Motherboard - CH641P-R0-1V0

1. Main control MCU: CH641P
2. LED: Wireless charging working status LED.
3. USB-C interface: The USB-C interface is used for power supply to support 9V, 12V, fast charging protocol handshake, and PB0 (CC1) is a SWDIO port. After mounting, the CC1 pin that leads to the USB-C can be upgraded through Link.
4. Bridge drive circuit: Drive high power load.
5. CBB resonant capacitor: Realize the resonance between primary and secondary circuits and improve the efficiency of power transmission.
6. Coil: Wireless charging A11 coil.

Tips: When burning the program, you need to disable the RST reset pin.

### 3. Software Development

#### 3.1 EVT Package Directory Structure



Description:

PUB folder: Provides evaluation board manuals.

EXAM folder: Provides software development drivers and corresponding examples for the CH641 controller, grouped by peripheral. Each type of peripheral folder contains one or more functional application routines folders.

#### 3.2 IDE Use - MounRiver

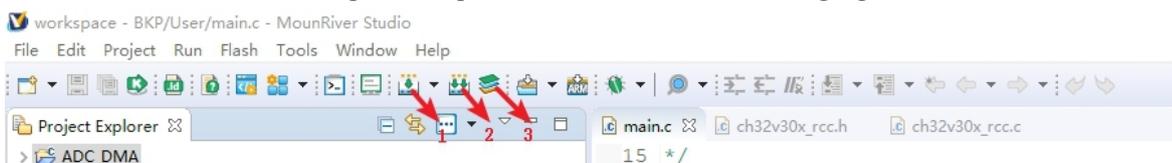
Download MounRiver\_Studio, double click to install it, and you can use it after installation. (MounRiver\_Studio instructions are available at the path: MounRiver\MounRiver\_Studio\ MounRiver\_Help.pdf and MounRiver\_ToolbarHelp.pdf)

##### 3.2.1 Open Project

- Open project:
  - 1) Double-click project file directly with the suffix name .wvproj under the corresponding project path.
  - 2) Click File in MounRiver IDE, click Load Project, select the .project file under the corresponding path, and click Confirm to apply it.

##### 3.2.2 Compilation

MounRiver contains three compilation options, as shown in the following figure.



Compile option 1 is Incremental Build, which compiles the modified parts of the selected project.

Compile option 2 is ReBuild, which performs a global compilation of the selected project.

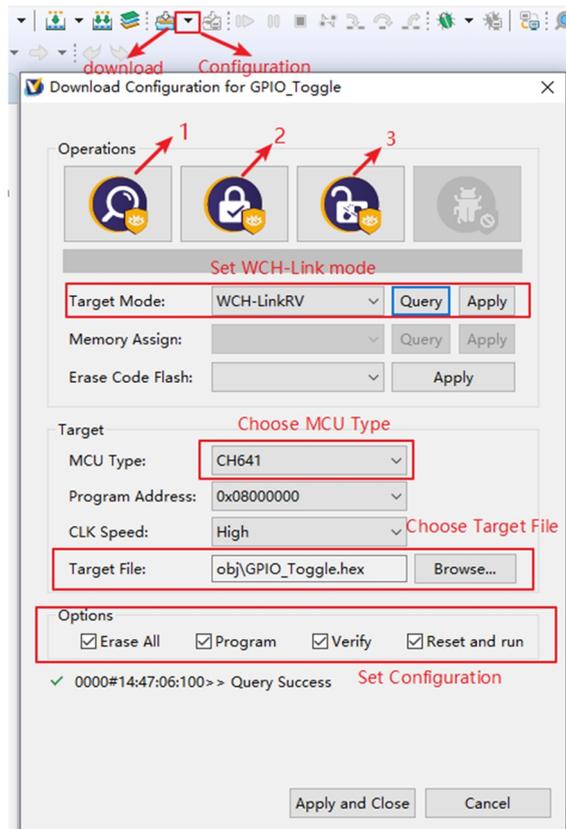
Compile option 3 is All Build, which performs global compilation for all projects.

### 3.2.3 Download/Simulation

➤ Download

1) Debugger download

Connect to the hardware via WCH-Link (see WCH-Link instructions for details, path: MounRiver\MounRiver\_Studio\ WCH-Link instructions.pdf), click the Download button on the IDE, and select Download in the pop-up interface, as shown in the figure below.



- 1 For querying the chip read protection status.
- 2 For setting the chip read protection and re-powering the configuration to take effect.
- 3 For lifting the chip read protection and re-powering the configuration to take effect.

➤ Simulation

1) Toolbar description

Click Debug button in the menu bar to enter the download mode, see the image below, the download toolbar.



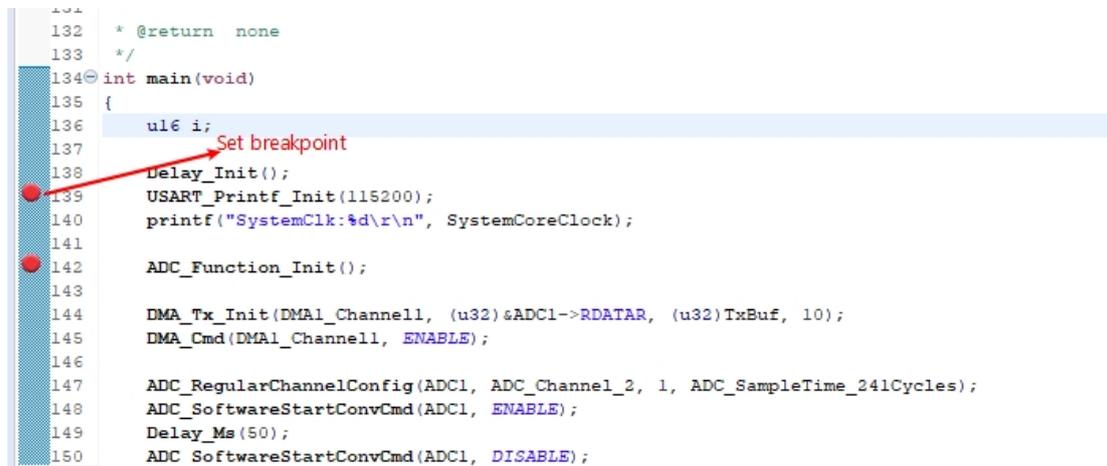
Detailed functions are as follows.

- (1) Reset: After reset, the program returns to the very beginning.
- (2) Continue: Click to continue debugging.
- (3) Terminate: Click to exit debugging.
- (4) Single-step jump-in: Each time you tap a key, the program runs one step and encounters a function to enter and execute.
- (5) Single-step skip: jump out of the function and prepare the next statement.
- (6) Single-step return: return the function you jumped into

Instruction set single-step mode: click to enter instruction set debugging (need to use with 4, 5 and 6 functions).

## 2) Set breakpoints

Double-click on the left side of the code to set a breakpoint, double click again to cancel the breakpoint, set the breakpoint as shown in the following figure;



```

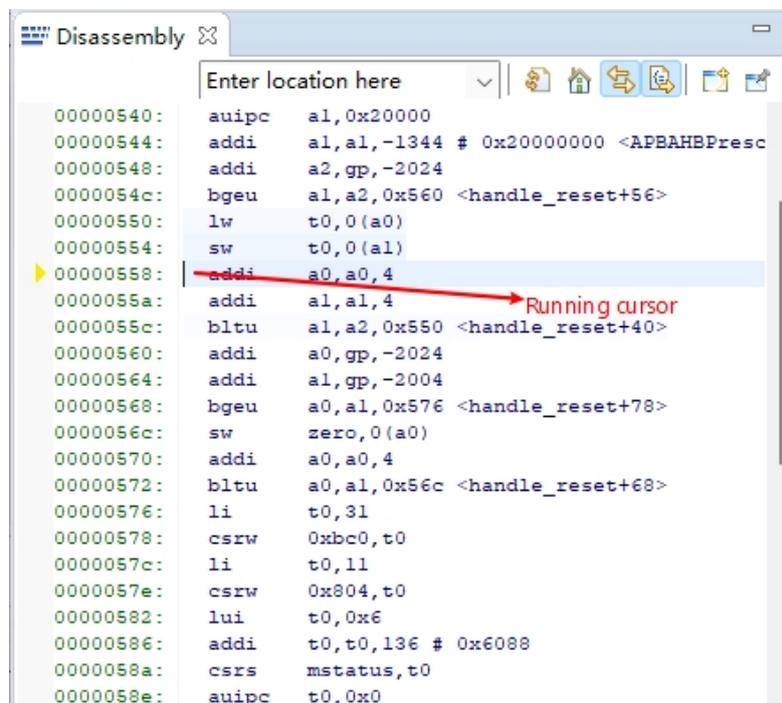
131
132 * @return none
133 */
134 int main(void)
135 {
136     ul6 i;
137
138     Delay_Init();
139     USART_Printf_Init(115200);
140     printf("SystemClk:%d\r\n", SystemCoreClock);
141
142     ADC_Function_Init();
143
144     DMA_Tx_Init(DMA1_Channel1, (u32)&ADC1->RDATAR, (u32)TxBuf, 10);
145     DMA_Cmd(DMA1_Channel1, ENABLE);
146
147     ADC_RegularChannelConfig(ADC1, ADC_Channel_2, 1, ADC_SampleTime_241Cycles);
148     ADC_SoftwareStartConvCmd(ADC1, ENABLE);
149     Delay_Ms(50);
150     ADC_SoftwareStartConvCmd(ADC1, DISABLE);

```

## 3) Interface display

### (1) Instruction set interface

Click on the instruction set single-step debugging can enter the instruction debugging, to single-step jump in for example, click once to run once, the running cursor will move to view the program running, the instruction set interface is shown as follows.



```

Disassembly
Enter location here
00000540: auipc  a1, 0x20000
00000544: addi   a1, a1, -1344 # 0x20000000 <APBAHBPresc
00000548: addi   a2, gp, -2024
0000054c: bgeu   a1, a2, 0x560 <handle_reset+56>
00000550: lw     t0, 0(a0)
00000554: sw     t0, 0(a1)
00000558: addi   a0, a0, 4
0000055a: addi   a1, a1, 4
0000055c: bltu   a1, a2, 0x550 <handle_reset+40>
00000560: addi   a0, gp, -2024
00000564: addi   a1, gp, -2004
00000568: bgeu   a0, a1, 0x576 <handle_reset+78>
0000056c: sw     zero, 0(a0)
00000570: addi   a0, a0, 4
00000572: bltu   a0, a1, 0x56c <handle_reset+68>
00000576: li     t0, 31
00000578: csrw   0xbc0, t0
0000057c: li     t0, 11
0000057e: csrw   0x804, t0
00000582: lui    t0, 0x6
00000586: addi   t0, t0, 136 # 0x6088
0000058a: csrs   mstatus, t0
0000058e: auipc  t0, 0x0

```

### (2) Program running interface

It can be used with instruction set single-step debugging, still take single-step jumping in as an example, click once to run once, the running cursor will move to view the program running, the program running interface is shown as follows.

```

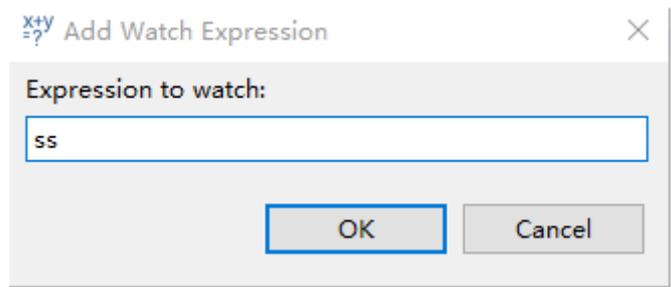
46  -
47  * @return none
48  */
49  int main(void)
50  {
51      u8 i = 0;
52
53      NVIC_PriorityGroupConfig(NVIC_PriorityGroup_2);
54      Delay_Init();
55      USART_Printf_Init(115200);
56      printf("SystemClk:%d\r\n", SystemCoreClock);
57
58      printf("GPIO Toggle TEST\r\n");
59      GPIO_Toggle_INIT();
60
61      while(1)
62      {
63          Delay_Ms(250);
64          GPIO_WriteBit(GPIOD, GPIO_Pin_0, (i == 0) ? (i = Bit_SET) : (i = Bit_RESET));
65      }
66  }
67

```

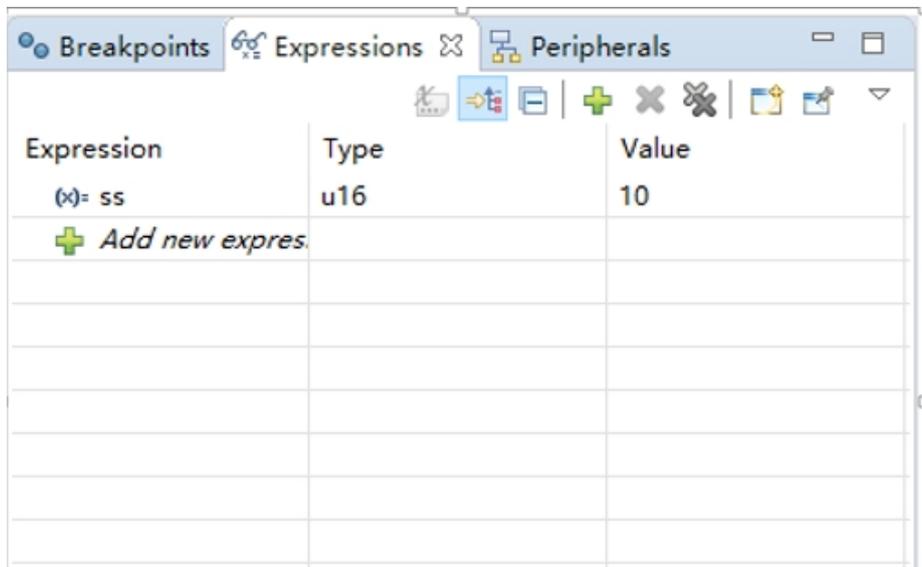
Running cursor

#### 4) Variables

Hover over the variable in the source code to display the details, or select the variable and right-click add watch expression

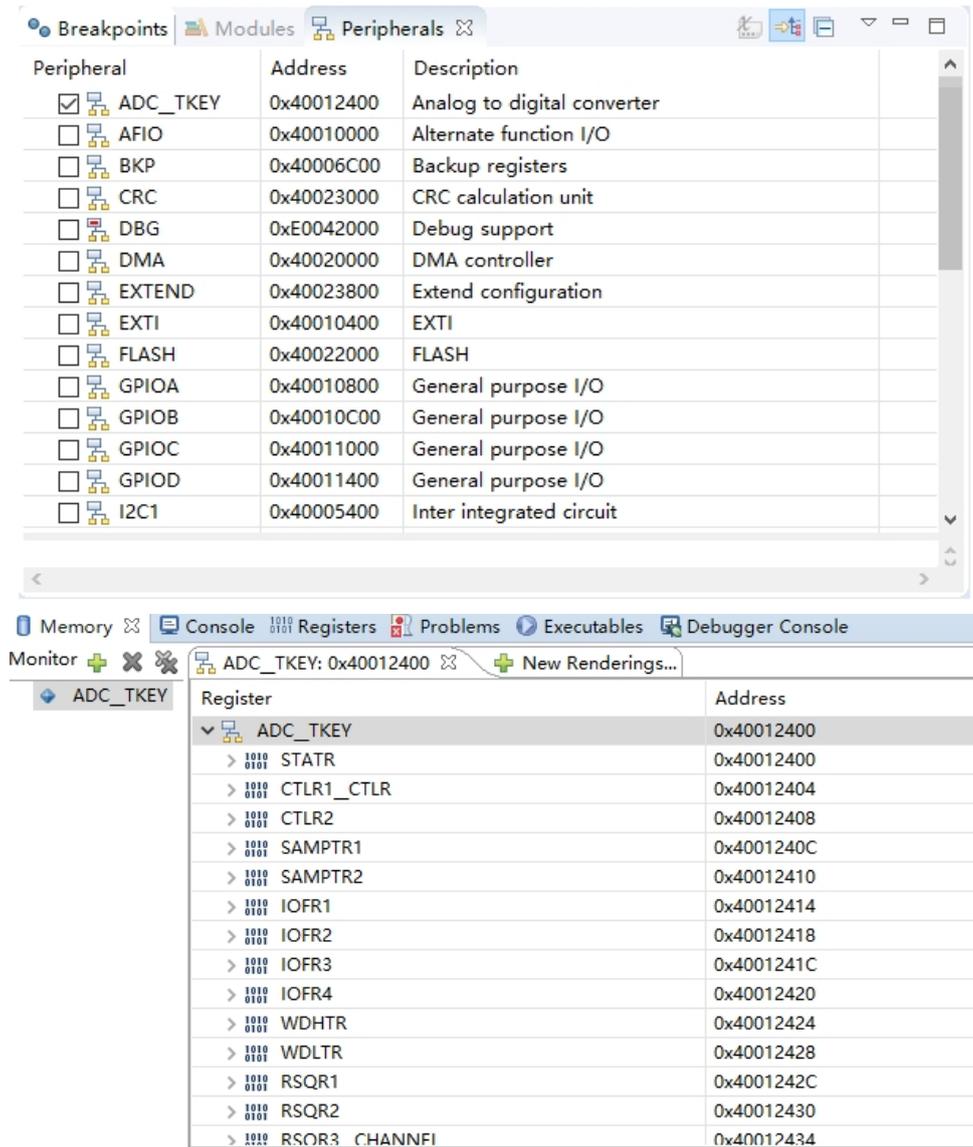


Fill in the variable name, or just click OK to add the variable you just selected to the pop-up.



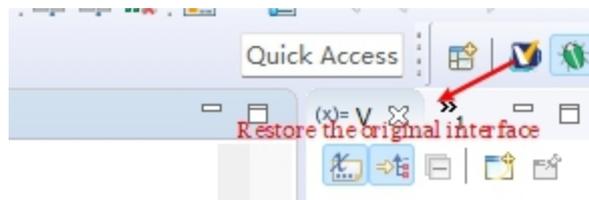
#### 5) Peripheral registers

In the lower left corner of IDE interface Peripherals interface shows a list of peripherals, tick the peripherals will display its specific register name, address, value in the Memory window.



Note:

(1) When debugging, click the icon in the upper right corner to enter the original interface.

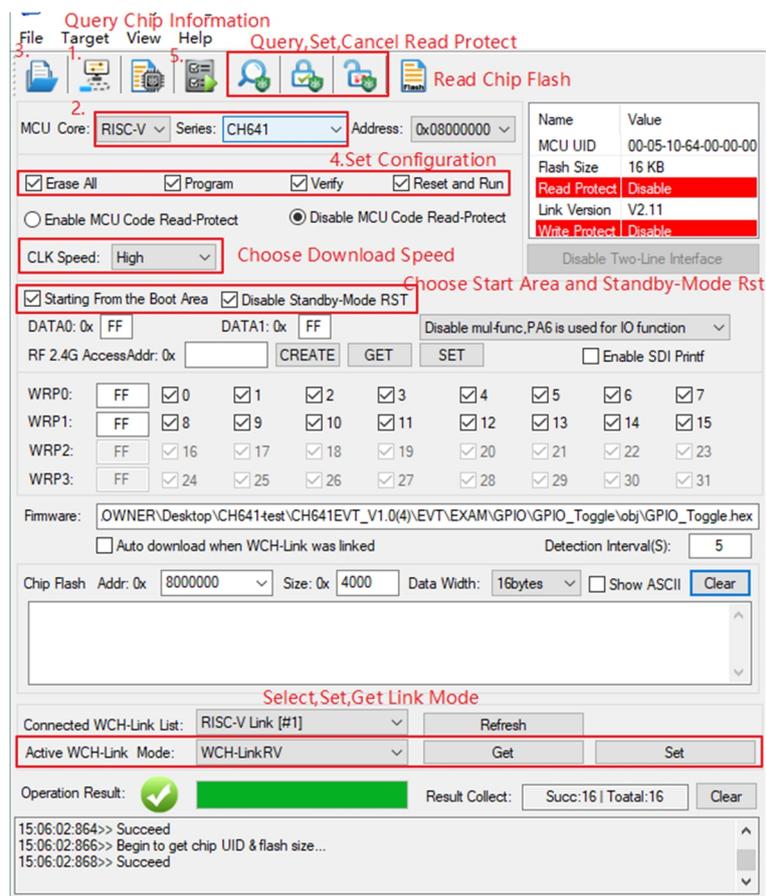


(2) For documentation to access the compiler, click F1 to access the help documentation for detailed instructions.

## 4. WCH-LinkUtility.exe Download

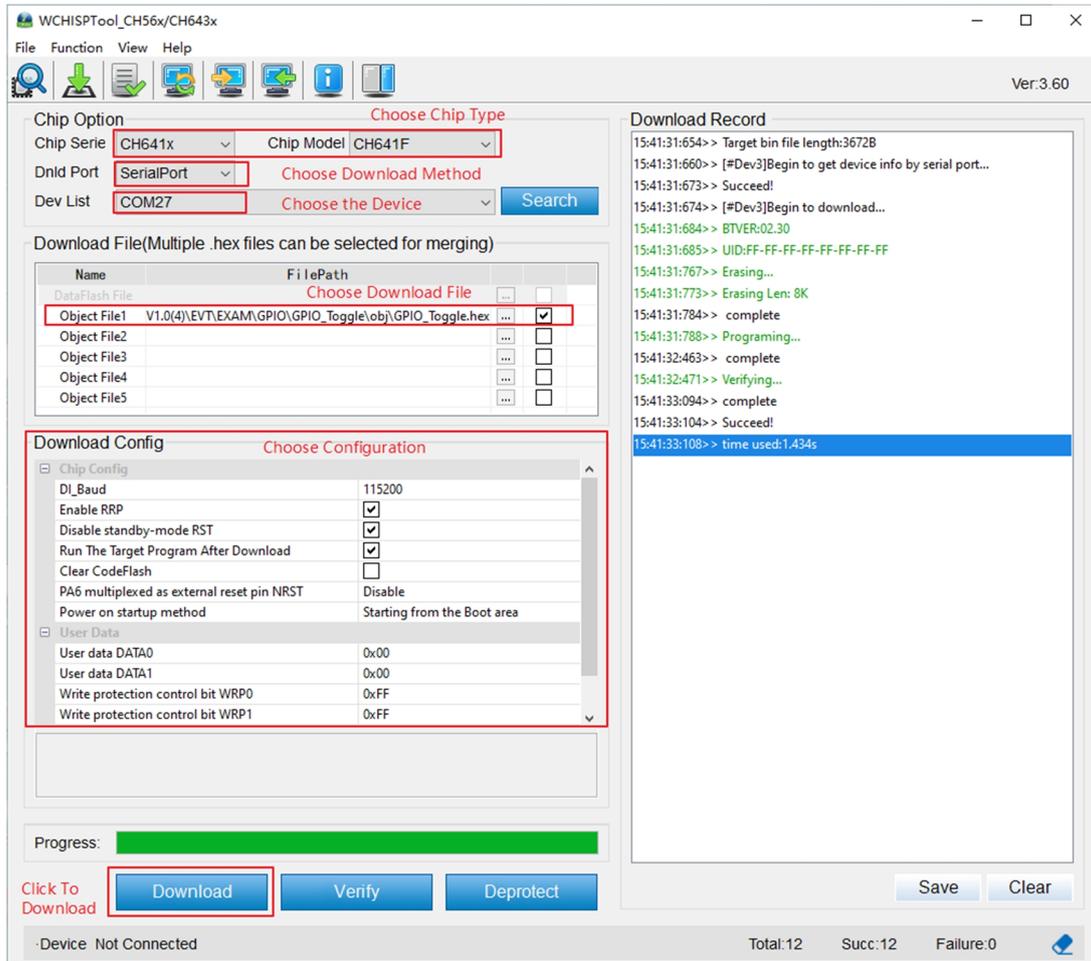
The download process for the chip using the WCH-LinkUtility tool is:

- 1) Connect WCH-Link
- 2) Select chip information
- 3) Add firmware
- 4) If the chip is read protected, you need to release the chip read protection.
- 5) Execute



## 5. WCHISPTool.exe Download

Use WCHISPTool to download the chip, only support serial port download (PA0, PA1). This tool is only used for IAP upgrade, the IAP program is solidified in the BOOT, users can jump to the BOOT by executing in the user area and use this tool to realize IAP upgrade. Refer to the IAP routine in EVT for the specific implementation method. The WCHISPTool tool interface is shown in the figure:



1. Select MCU series and chip model
2. Select the serial port download mode
3. Identify the device, usually automatically, if it fails to identify, you need to select manually
4. Select the firmware, select the downloaded .hex or .bin target program file
5. Configure the download according to the requirements
6. Click download

## 6. Statement of Attention

- 1) If you are using WCH-Link to download, refer to the WCH-LinkUserManual for the specific method of switching modes.

Detailed inquiries\questions can be logged in the following.

WCH official website: <https://www.wch-ic.com/>

WCH-LINK instructions for use: <https://www.wch-ic.com/products/WCH-Link.html>