CH32V 系列

注意:

1、我们 EVT 例程中默认配置是 CH32V203C8T6 配置,若使用 MCU 为 CH32V203K8T6-CH32V203C8U6-CH32V203C8T6,无需修改配置

2、若使用 MCU 为 CH32V203RBT6 或 CH32V208 系列,注意使用外部晶振大小为 32MHz

1、MCU 型号为:

CH32V203F6P6-CH32V203G6U6-CH32V203K6T6-CH32V203C6T6 (FLASH: 32K+RAM: 10K) CH32V203K8T6-CH32V203C8U6-CH32V203C8T6 (FLASH: 64K+RAM: 20K)

(1) 修改 ch32v20x.h 文件中宏定义。如下图圈出部分,根据芯片型号,选择对应的宏 定义

ⓒ main.c h ch32v20x.h ⊠	
10/*********************	(C) COPYRIGHT *********************
2 * File Name : ch32v20x	c.h
3 * Author : WCH	
4 * Version : V1.0.0	
5 * Date : 2021/06/	/06
6 * Description : CH32V202	Device Peripheral Access Layer Header File.
7 * Copyright (c) 2021 Nanjing Qi	inheng Microelectronics Co., Ltd.
8 * SPDX-License-Identifier: Apac	che-2.0
g ***********************	***************************************
10 <sup>©</sup> #ifndef CH32V20x H	
11 #define CH32V20x H	
12	
13 <sup>©</sup> #ifdefcplusplus	
14 extern "C" {	
15 #endif	
16	
17 #define CH32V20x_D6	/* CH32V203C6T6-CH32V203K8T6-CH32V203C8U6-CH32V203C8T6 */
188//#define CH32V20x_D8	/* CH32V203RBT6 */
19 //#define CH32V20x_D8W	/* CH32V208 */
20	
21 #defineMPU_PRESENT	0 /* Other CH32 devices does not provide an MPU */
<pre>22 #defineVendor_SysTickConfig</pre>	0 /* Set to 1 if different SysTick Config is used */
23	
240 #if defined(CH32V20x_D8)    defi	ned (CH32V20x_D8W)
25 #define HSE_VALUE ((uint32	t)32000000) /* Value of the External Oscillator in Hz */
260 #else	
27 #derine HSE_VALUE ((uint32_	t)8000000) /* value of the External oscillator in Hz */
28 #enair	

(2)修改启动文件(.s文件)。如下图,选中启动文件,点击鼠标右键,选择或屏蔽 该启动文件。如何选择启动文件根据宏定义来选择

Project Explorer 🛛	🕒 😫 🗽 🔻 🗖	💽 main.c 🛛 🕞 ch32	w20x.h ⊠
😽 GPIO Toggle		10/*******	*******
> 🔊 Includes		2 * File N	Iame : ch32v20x
> 🔐 Core		3 * Author	: WCH
> 🔗 Debug		4 * Versio	on : V1.0.0
> 🔗 Ld		5 * Date	: 2021/06/
> Peripheral		7 * Copyri	ght (c) 2021 Nanjing Oi
		8 * SPDX-L	icense-Identifier: Apac
S startup ch32v20x D6.S		9 ******	*****************
j∰ startup_ch32v20x_D8.S	New	>	CH32V20x H CH32V20x H
j∰ startup_ch32v20x_D8W	Open		-
✓ ➢ User	Open With	>	plusplus
ch32v20x_it.c	Сору	Ctrl+C	
h ch32v20x_it.h	Paste	Ctrl+V	
💽 main.c 🛛 🗙	Remove	Delete	132V20x_D6
system_ch32v20x.c	Move		CH32V20X_D8
system_ch32v20x.h	Rename	F2	CHOZYZOR_DOW
	Build Project	F7	MPU_PRESENT Vendor SysTickConfig
Ð	Refresh	F5	
è	Download	>	d(CH32V20x_D8)    defi HSE_VALUE ((uint32
1. S	Include/Exclude From Build		HSE VALUE ((uint32
(III)	Code Format		
No.	Import		following line adjust
1	Export		SE_STARTUP_TIMEOUT

(3) 修改 ld 文件.ld 文件中,主要修改 FLASH 和 RAM 的大小,具体大小根据所选 MCU 实际大小修改,此处以 CH32V203F6P6 (FLASH-32K, RAM-10K)为例,具体修改如下图:

```
E Link.ld
         h ch32v20x.h
.c main.c
    l ENTRY( _start )
    2
    3 __stack_size = 2048;
    4
    5 PROVIDE( _stack_size = __stack_size );
    6
    7
   8 MEMORY
   9 {
   10 /* CH32V20x D6 - CH32V203F6-CH32V203G6-CH32V203K6-CH32V203C6 */
   11 /**/
  12
         FLASH (rx) : ORIGIN = 0x00000000, LENGTH = 32K
         RAM (xrw) : ORIGIN = 0x20000000, LENGTH = 10K
   13
  14
  15
  16 /* CH32V20x D6 - CH32V203K8-CH32V203C8-CH32V203G8-CH32V203F8 */
  17/*
   18
         FLASH (rx) : ORIGIN = 0x00000000, LENGTH = 64K
   19
         RAM (xrw) : ORIGIN = 0x20000000, LENGTH = 20K
   20 */
   21
  22 /* CH32V20x D8 - CH32V203RB
       CH32V20x D8W - CH32V208x
  23
        FLASH + RAM supports the following configuration
  24
        FLASH-128K + RAM-64K
   25
   26
        FLASH-144K + RAM-48K
        FLASH-160K + RAM-32K
  27
  28
  29
         FLASH (rx) : ORIGIN = 0x00000000, LENGTH = 160K
  30
         RAM (xrw) : ORIGIN = 0x20000000, LENGTH = 32K
  31 */
32 }
```

## 2、MCU 型号为: CH32V203RBT6(FLASH: 128K+RAM: 32K)

(1)修改 ch32v20x.h 文件中宏定义。如下图圈出部分,根据芯片型号,选择对应的宏

```
定义
```

main.c 🕞 ch32v20x.h 🖾 📑 Lir	nk.ld	
10/**************	************** (C) COPYRIGH	. ***********************
2 * File Name	: ch32v20x.h	
3 * Author	: WCH	
4 * Version	: V1.0.0	
5 * Date	: 2021/06/06	
6 * Description	: CH32V20x Device Periphera	al Access Layer Header File.
7 * Copyright (c) 2021	Nanjing Qinheng Microelect:	conics Co., Ltd.
8 * SPDX-License-Ident.	ifier: Apache-2.0	
9 ************	* * * * * * * * * * * * * * * * * * * *	********
130 #ifdefcplusplus 14 extern "C" { 15 #endif		
16		
16 17 //#define CH32V20x_D6	/* CH32V203C6	<u>16-CH</u> 32V203K8T6-CH32V203C8U6-CH32V203C8T6 */
16 17 //#define CH32V20x_D6 18 #define CH32V20x_D8	/* CH32V203C6 /* CH32V203RBT6	16-CH32V203K8T6-CH32V203C8U6-CH32V203C8T6 */
16 17 //#define CH32V20x_D6 18 #define CH32V20x_D8 19 //#define CH32V20x_D8	/* CH32V203C6 /* CH32V203RBT6 W /* CH32V208 *,	16-CH32V203K8T6-CH32V203C8U6-CH32V203C8T6 */ */
16 17 //#define CH32V20x D6 18 #define CH32V20x_D8 19 //#define CH32V20x_D8 20	/* CH32V203C67 /* CH32V203RBT6 W /* CH32V208 *,	<u>r6-CH</u> 32V203K8T6-CH32V203C8U6-CH32V203C8T6 */ *∕
16 17 //#define CH32V20x_D6 18 #define CH32V20x_D8 19 //#define CH32V20x_D8 20 21 #defineMPU_PRESENT	/* CH32V203C67 /* CH32V203RBT6 W /* CH32V208 *, 0	<pre>[6-CH32V203K8T6-CH32V203C8U6-CH32V203C8T6 *, */ /* Other CH32 devices does not provide</pre>
16 17 //#define CH32V20x_D6 19 #define CH32V20x_D8 19 //#define CH32V20x_D8 20 21 #defineMPU_PRESENT 22 #defineVendor_SysT	/* CH32V203C6 /* CH32V203RBT6 W /* CH32V208 *, 0 ickConfig 0	<pre>r6-CH32V203K8T6-CH32V203C8U6-CH32V203C8T6 * */ /* Other CH32 devices does not provid /* Set to 1 if different SysTick Conf.</pre>

(2)修改启动文件(.s文件)。如下图,选中启动文件,点击鼠标右键,选择或屏蔽 该启动文件。如何选择启动文件根据宏定义来选择

Project Explorer 🛛		🕒 🧐 🛃 🗸 🖓 🗖	C main.c	h ch3.	2v20x.h 🛛 📄 Link.ld	
<ul> <li>✓ GPIO_Toggle</li> <li>&gt; ∰ Binaries</li> <li>&gt; ∭ Includes</li> <li>&gt; ∰ Core</li> <li>&gt; @ Debug</li> <li>✓ @ Debug</li> <li>✓ @ Link.ld</li> <li>&gt; @ Startup</li> <li>✓ Entertum ele 20:00.00</li> </ul>	-		1⊕ 2 3 4 5 6 7 8 9 10⊕ 11	<pre>/******* * File 1 * Autho: * Versi* Date * Descr: * Copyr: * SPDX-1 ******* #ifndef #define</pre>	Name : c r : W on : V on : C igtion : C igtht (c) 2021 <u>Nam</u> License-Identifier <u>CH32V20x_H</u>	********* (C) COPYRIGHT ** h32v20x.h CH 1.0.0 021/06/06 H32V20x Device Peripheral Ac Jing Qinheng Microelectronic r: <u>Apache</u> -2.0
<ul> <li>is startup_ch32v20x_D6</li> <li>is startup_ch32v20x_D6</li> <li>is startup_ch32v20x_D8</li> <li>is obj</li> <li>is User</li> <li>in ch32v20x_conf.h</li> <li>is ch32v20x_it.c</li> <li>in ch32v20x_it.h</li> <li>is ch32v20x_it.h</li> <li>is main.c</li> <li>is matter ab23v20 a c</li> </ul>		New Open Open With Copy Paste Remove Move		> Ctrl+C Ctrl+V Delete	cplusplus ( CH32V20x_D6 H32V20x_D8 CH32V20x_D8 MPU_PRESENT Vendor SysTickC	/* CH32V203C6T6-CH /* CH32V203RBT6 */ /* CH32V208 */ 0 000fig 0
€ system_ch32v20x.c ♪ system_ch32v20x.h	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	Rename Build Project Refresh Download		F2 F7 F5	ed (CH32V20x_D8) HSE_VALUE (() HSE_VALUE ()	<pre>   defined(CH32V20x_D8W) uint32_t)32000000) /* Value uint32_t)8000000) /* Value c</pre>
		Include/Exclude From Buil Code Format Import Export	d		following line SE_STARTUP_TIMEO SI_VALUE	adjust the External High Spe UT ((uint16_t)0x1000) /* ((uint32_t)8000000) /*

(3) 修改 ld 文件.ld 文件中,主要修改 FLASH 和 RAM 的大小,具体大小根据所选 MCU 实际大小修改,此处以 CH32V203RBT6 (FLASH-128K, RAM-32K)为例,具体修改如下 图:

```
🖻 main.c 🔥 ch32v20x.h 📄 Link.ld ⊠
   l ENTRY( _start )
   2
   3 __stack_size = 2048;
   5 PROVIDE ( stack size = stack size );
   6
   8 MEMORY
   9 {
   10 /* CH32V20x D6 - CH32V203F6-CH32V203G6-CH32V203K6-CH32V203C6 */
  11/*
   12
         FLASH (rx) : ORIGIN = 0x00000000, LENGTH = 32K
         RAM (xrw) : ORIGIN = 0x20000000, LENGTH = 10K
  13
   14*/
  15
  16 /* CH32V20x D6 - CH32V203K8-CH32V203C8-CH32V203G8-CH32V203F8 */
  17/*
         FLASH (rx) : ORIGIN = 0x00000000, LENGTH = 64K
  18
  19
         RAM (xrw) : ORIGIN = 0x20000000, LENGTH = 20K
  20 */
   21
  22 /* CH32V20x D8 - CH32V203RB
  23 CH32V20x D8W - CH32V208x
  24
       FLASH + RAM supports the following configuration
      FLASH-128K + RAM-64K
  25
  26 FLASH-144K + RAM-48K
  27
       FLASH-160K + RAM-32K
   28 */
        FLASH (rx) : ORIGIN = 0x00000000, LENGTH = 128K
  29
       RAM (xrw) : ORIGIN = 0x20000000, LENGTH = 32K
  30
  31
  32 }
  33
  34
```

### 3、MCU 型号为: CH32V208 系列(FLASH: 128K+RAM: 64K)

(1) 修改 ch32v20x.h 文件中宏定义。如下图圈出部分,根据芯片型号,选择对应的宏

定义

4	* Version :	V1.0.0	
5	* Date :	2021/06/06	
6	* Description :	CH32V20x Device Peripheral	Access Layer Header File.
7	* Copyright (c) 2021 Na	njing Qinheng Microelectro	nics Co., Ltd.
8	* SPDX-License-Identifi	ler: Apache-2.0	
9	* * * * * * * * * * * * * * * * * * * *	*******************	*******
100	#ifndef CH32V20x H		
11	#define CH32V20x H		
12			
130	#ifdef cplusplus		
14	extern "C" {		
15	#endif		
16			
	//#define CH32V20x D6	/* CH32V203C6T6	-CH32V203K8T6-CH32V203C8U6-CH32V203C8T6 */
170			- 1
179 18	//#define CH32V20x_D8	/* (H37/203PBTE	<b>1</b> °/
179 18	//#define CH32V20x_D8 #define CH32V20x_D8W	/* CH32V203BBTE /* CH32V208 */	1.7
179 18 19 20	//#define CH32V20x_D8 #define CH32V20x_D8W	/* CH32V203BBTE /* CH32V208 */	]^
17⊖ 18 19 20 21	//fdefine_CH32V20x_D8 #define_CH32V20x_D8W #defineMPU_PRESENT	/* CH32V203DETE /* CH32V208 */ 0	/* Other CH32 devices does not provide a

(2)修改启动文件(.s文件)。如下图,选中启动文件,点击鼠标右键,选择或屏蔽 该启动文件。如何选择启动文件根据宏定义来选择

눱 Project Explorer 🛛	= 🔄 🤖 🗸 🗖 🗖	C main.c	h ch32v	20x.h 🖾
<ul> <li>✓ Sig GPIO_Toggle</li> <li>&gt; ∰ Binaries</li> <li>&gt; in Includes     <li>&gt; in Core     <li>&gt; in Debug     <li>&gt; in Ld     <li>&gt; in Peripheral     <li>✓ in Startup   </li> </li></li></li></li></li></ul>		4 * 5 * 6 * 7 * 8 * 9 ** 10⊕ #if 11 #de 12	Version Date Descrip Copyric SPDX-Li ******* indef fine	otion ght (c) 2021 Leense-Identi CH32V20x_H CH32V20x_H
startup_ch32v20x_D8W.^	New	13⊖ <b>#if</b>	def o	cplusplus
startup_ch32v20x_D8.S     bj     o    bj	Open Open With		>	CH32V20x_D6
ber     ber     ch32v20x_conf.h     ch32v20x_it.c     b ch32v20x_it.h	Copy Paste Remove		Ctrl+C Ctrl+V Delete	32V20x_D8W 1PU_PRESENT Vendor_SysTi
c main.c c system_ch32v20x.c	Rename		F2	1(CH32V20x_E HSE VALUE
	Build Project Refresh		F7 F5	ISE_VALUE
	Download Include/Exclude From E Code Format	Build		Following li STARTUP_TI
	Import Export			I_VALUE pt Number De
	Translate Selected File(	s)		am IRQn

(3) 修改 ld 文件.ld 文件中,主要修改 FLASH 和 RAM 的大小,具体大小根据所选 MCU 实际大小修改,此处以 CH32V203RBT6 (FLASH-128K, RAM-64K)为例,具体修改如下 图:

```
🖻 main.c 🚯 ch32v20x.h 📄 Link.ld ⊠
   l ENTRY( _start )
   2
   3 __stack_size = 2048;
   5 PROVIDE( _stack_size = __stack_size );
   6
   7
   8 MEMORY
   9 {
   10 /* CH32V20x D6 - CH32V203F6-CH32V203G6-CH32V203K6-CH32V203C6 */
  11/*
  12
        FLASH (rx) : ORIGIN = 0x00000000, LENGTH = 32K
        RAM (xrw) : ORIGIN = 0x20000000, LENGTH = 10K
   13
  14*/
  15
  16 /* CH32V20x D6 - CH32V203K8-CH32V203C8-CH32V203G8-CH32V203F8 */
  17/*
        FLASH (rx) : ORIGIN = 0x00000000, LENGTH = 64K
  18
  19
        RAM (xrw) : ORIGIN = 0x20000000, LENGTH = 20K
  20 */
   21
  22 /* CH32V20x D8 - CH32V203RB
   23 CH32V20x_D8W - CH32V208x
  24 FLASH + RAM supports the following configuration
   25
       FLASH-128K + RAM-64K
  26
      FLASH-144K + RAM-48K
   27
       FLASH-160K + RAM-32K
   28
       FLASH (rx) : ORIGIN = 0x00000000, LENGTH = 128K
   29
        RAM (xrw) : ORIGIN = 0x20000000, LENGTH = 64K
   30
   31
  32 }
  33
```

CH32F 系列

注意:

1、我们 EVT 例程中默认配置是 CH32F203C8T6 配置,若使用 MCU 为
 CH32F203C6T6\CH32F203K8T6-CH32F203C8T6-CH32F203C8U6,无需修改配置
 2、若使用 MCU 为 CH32F208 系列,注意使用外部晶振大小为 32MHz

1、MCU 型号为:

#### CH32F203C6T6 (FLASH: 32K+RAM: 10K)

#### CH32F203K8T6-CH32F203C8T6-CH32F203C8U6 (FLASH: 64K+RAM: 20K)

(1) 修改 ch32F20x.h 文件中宏定义。如下图圈出部分,根据芯片型号,选择对应的宏 定义

ch3	2F20x.h	
1	-/**************	************** (C) COPYRIGHT *********************************
2	* File Name :	ch32f20x.h
3	* Author :	WCH
4	* Version :	V1.0.0
5	* Date :	2021/08/08
6	* Description :	CH32F20x Device Peripheral Access Layer Header File.
7	* Copyright (c) 2021 N	Nanjing Qinheng Microelectronics Co., Ltd.
8	* SPDX-License-Identif	ier: Apache-2.0
9	L**************	* * * * * * * * * * * * * * * * * * * *
10	=#ifndef CH32F20x H	
11	#define CH32F20x H	
12		
13	= #ifdefcplusplus	
14	extern "C" {	
15	#endif	
16	-	
17	<pre>#define CH32F20x_D6</pre>	/* CH32F203K8-CH32F203C6-CH32F203C8 */
18	//#define CH32F20x_D8	/* CH32F203CB-CH32F203RC-CH32F203VC */
19	//#define CH32F20x_D8C	/* CH32F207x-CH32F205x */
20	//#define CH32F20x_D8W	/* CH32F208x */
21		
22	#defineMPU_PRESENT	0 /* Other CH32 devices does not provide a
23	#defineNVIC_PRIO_BI	TS 4 /* CH32 uses 4 Bits for the Priority Lev
24	#defineVendor_SysTi	.ckConfig 0 /* Set to 1 if different SysTick Config

(2)修改启动文件。更改 startup 文件,点击 Manage Project Items 进行文件更换,选用 startup\_ch32f20x\_D6.s 文件,进行替换,如下图

Project 📮 🗵	ch32F20x.h
Project  Project: CH32F20x CH3	Ch32F20x.h      1
🛛  debug 🕢 🧰 peripheral	<pre>8 * SPDX-License-Identifier: Apa 9 ***********************************</pre>
	20 //#define CH32F20x_D8W 21

2、MCU 型号为:

CH32F203CBT6 (FLASH: 128K+RAM: 32K)

## CH32F203RCT6-CH32F203VCT6 (FLASH: 256K+RAM: 64K)

(1)修改 ch32F20x.h 文件中宏定义。如下图圈出部分,根据芯片型号,选择对应的宏

定义

ch32	2F20x.h	
		***************** (C) COPYRIGHT ************************************
2	* File Name	: ch32f20x.h
3	* Author	: WCH
4	* Version	: V1.0.0
5	* Date	: 2021/08/08
6	* Description	: CH32F20x Device Peripheral Access Layer Header File.
7	* Copyright (c) 2021	Nanjing Qinheng Microelectronics Co., Ltd.
8	* SPDX-License-Identi	fier: Apache-2.0
9	*************	***************************************
10	findef CH32F20x H	
11	#define CH32F20x H	
12		
13	<b>#ifdef</b> cplusplus	
14	extern "C" {	
15	#endif	
16		
17	//#define CH32E20x D6	/* CH32F203K8-CH32F203C6-CH32F203C8 */
18	#define CH32F20x D8	/* CH32F203CB-CH32F203RC-CH32F203VC */
19	//#define CH32F20x D8	C /* CH32F207x-CH32F205x */
20	//#define CH32F20x D8	W /* CH32F208x */
21		
22	#define MPU PRESENT	0 /* Other CH32 devices does not provide an MPU
23	#define NVIC PRIO B	ITS 4 /* CH32 uses 4 Bits for the Priority Levels *
24	#define Vendor SysT.	ickConfig 0 /* Set to 1 if different SysTick Config is us
25		

(2)修改启动文件。更改 startup 文件,点击 Manage Project Items 进行文件更换,选用 startup\_ch32f20x\_D8.s 文件,进行替换,如下图

Project 🎝	Ch32F20x.h	
<ul> <li>Project: CH32F20x</li> <li>CH32F20x</li> <li>user</li> <li>startup</li> <li>startup_ch32f20x_D8.s</li> <li>core</li> <li>debug</li> <li>peripheral</li> </ul>	<pre> 1 = /***********************************</pre>	<pre>: ch32f20x.h : ch32f20x.h : WCH : V1.0.0 : 2021/08/08 : CH32F20x Device Peripheral Acce: 021 Nanjing Qinheng Microelectronics ( lentifier: Apache-2.0 x_H bx_H bx_H bx_H bx_H bx_H bx_H bx_H</pre>

# 3、MCU 型号为:

CH32F205xx (FLASH: 128K+RAM: 32K)

## CH32F207xx (FLASH: 256K+RAM: 64K)

(1)修改 ch32F20x.h 文件中宏定义。如下图圈出部分,根据芯片型号,选择对应的宏

定义

ch3	2F20x.h	
1	-/********	**** (C) COPYRIGHT ******************************
2	* File Name : ch32f20x	.h
3	* Author : WCH	
4	* Version : V1.0.0	
5	* Date : 2021/08/	08
6	* Description : CH32F20x	Device Peripheral Access Layer Header File.
7	* Copyright (c) 2021 Nanjing Qir	nheng Microelectronics Co., Ltd.
8	* SPDX-License-Identifier: Apac	he-2.0
9	L*************************************	*************
10	FinderCH32F20x_H	
11	<pre>#define CH32F20x H</pre>	
12		
13	🔁 <b>#ifdef</b> cplusplus	
14	🔁 extern "C" {	
15	#endif	
16		
17	//#define CH32F20x_D6	/* CH32F203K8-CH32F203C6-CH32F203C8 */
18	//#define CH32F20x_D8	/* CH32F203CB_CH32F203RC-CH32F203VC */
19	<pre>#define CH32F20x_D8C</pre>	/* CH32F207x-CH32F205x */
20	//#define CH32F20x_D8W	/* CH32F208x */
21		
22	<pre>#define MPU_PRESENT</pre>	0 /* Other CH32 devices does not provide a
23	<pre>#defineNVIC_PRIO_BITS</pre>	4 /* CH32 uses 4 Bits for the Priority Lev
24	<pre>#defineVendor_SysTickConfig</pre>	0 /* Set to 1 if different SysTick Config
25	and a stress of the life of the second secon	

(2)修改启动文件。更改 startup 文件,点击 Manage Project Items 进行文件更换,选用 startup\_ch32f20x\_D8C.s 文件,进行替换,如下图

Project 🛛 📮 🗵	ch32F20x.h
Project: CH32F20x CH32F20x User startup startup_ch32f20x_D8C.s core debug peripheral	<pre> 1 = /***********************************</pre>
	17         //#define CH32F20x_D6         /# C           18         //#define CH32F20x_D8         /# CH3           19         #define CH32F20x_D8         /# CH32F           20         //#define CH32F20x_D8W         /# CH32F

## 4、MCU 型号为:

### CH32F208xx (FLASH: 128K+RAM: 64K)

(1)修改 ch32F20x.h 文件中宏定义。如下图圈出部分,根据芯片型号,选择对应的宏

定义

1 4	]/**************	**********	**** (C) COPYRIGHT	******	
2	* File Name	: ch32f20x.	.h		
3	* Author	: WCH			
4	* Version	: V1.0.0			
5	* Date	: 2021/08/0	08		
6	* Description	: CH32F20x	Device Peripheral	Access Layer Header File.	
7	* Copyright (c) 202	1 Nanjing Qir	nheng Microelectro	nics Co., Ltd.	
8	* SPDX-License-Identifier: Apache-2.0				
9	_ * * * * * * * * * * * * * * * * * * *	*********	****	* * * * * * * * * * * * * * * * * * * *	
10 5	#ifndef _CH32F20x	Н			
11	<pre>#defineCH32F20x</pre>	H			
12					
13 E	#ifdefcplusplus				
14 -	I avtorn UCH I				
	J evcern c 1				
15	#endif				
15 16	fendif				
15 16 17	<pre>#endif //#define CH32F20x_</pre>	D6	/* CH32F203K	8-CH32F203C6-CH32F203C8 */	
15 16 17 18	<pre>#endif //#define CH32F20x_ //#define CH32F20x_</pre>	D6 D8	/* CH32F203K	8-CH32F203C6-CH32F203C8 */ CH32F203RC-CH32F203VC */	
15 16 17 18 19	<pre>#endif //#define CH32F20x //#define CH32F20x //#define CH32F20x</pre>	D6 D8 D8C	/* CH32F203K /* CH32F203CB- /* CH32F207X-CI	8-CH32F203C6-CH32F203C8 */ CH32F203RC-CH32F203VC */ 32F205x */	
15 16 17 18 19 20	//#define CH32F20x //#define CH32F20x //#define CH32F20x #define CH32F20x #define CH32F20x_D8	D6 D8 D8C	/* CH32F203K /* CH32F203CB- /* CH32F207X-CJ /* CH32F208x */	8-CH32F203C6-CH32F203C8 */ CH32F203RC-CH32F203VC */ M32F205x */	
15 16 17 18 19 20 21	<pre>#endif //#define CH32F20x_ //#define CH32F20x_ //#define CH32F20x_ #define CH32F20x_D8</pre>	D6 D8 D8C W	/* CH32F203K /* CH32F203CB-I /* CH32F207K-CI /* CH32F208x */	8-CH32F203C6-CH32F203C8 */ CH32F203RC-CH32F203VC */ 32F205x */	
15 16 17 18 19 20 21 22	<pre>#endif //#define CH32F20x_ //#define CH32F20x_ //#define CH32F20x_ #define CH32F20x_D8 #defineMPU_PRESE</pre>	D6 D8 D8C W NT	/* CH32F203K /* CH32F203CB- /* CH32F207X-CI /* CH32F208x */ 0 /* Other CH32	8-CH32F203C6-CH32F203C8 */ CH32F203RC-CH32F203VC */ 32F205x */ devices does not provide	
15 16 17 18 19 20 21 22 23	<pre>#endif //#define CH32F20x_ //#define CH32F20x_ 7/#define CH32F20x_ #define CH32F20x_D8 #defineMPU_PRESE #defineNVIC_PRIO</pre>	D6 D8 D8C W NT _BITS	/* CH32F203K /* CH32F203CB- /* CH32F207K-CI /* CH32F208x */ 0 /* Other CH32 4 /* CH32 uses	8-CH32F203C6-CH32F203C8 */ CH32F203RC-CH32F203VC */ 32F205x */ devices does not provide 4 Bits for the Priority Le	
15 16 17 18 19 20 21 22 23 24	<pre>#extern "C T #endif //#define CH32F20x_ //#define CH32F20x_ #define CH32F20x_D8 #defineMPU_PRESE #defineNVIC_PRIO #defineVendor_Sy</pre>	D6 D8 D8C W W NT _BITS sTickConfig	/* CH32F203K /* CH32F203CB- /* CH32F207X-CL /* CH32F208x */ 0 /* Other CH32 4 /* CH32 uses 0 /* Set to 1 i:	8-CH32F203C6-CH32F203C8 CH32F203RC-CH32F203VC */ 32F205x */ devices does not provide 4 Bits for the Priority I f different SysTick Confi	

(2)修改启动文件。更改 startup 文件,点击 Manage Project Items 进行文件更换,选用 startup\_ch32f20x\_D8W.s 文件,进行替换,如下图

Project 📮 🗵	ch32F20x.h
Project  Project: CH32F20x  CH32F20x	<pre>d32F20xh      d32F20xh      d32F20xh</pre>
	20         #define CH32F20x_DBW         /* CH32F208x */           21         22         #defineMPU_PRESENT         0 /* Other CH32 dev